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SECURITY ANALYSIS BETWEEN STATIC AND DYNAMIC S-BOXES IN BLOCK CIPHERS

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Abstract:

The development of block ciphers has resulted in a number of cryptographic algorithms such as AES, aria, blowfish256, desl, and 3d-aes. AES is one of the best cryptographic algorithms that can be used to protect electronic data. However, the principal weakness in AES is the linearity in the s-box. The objective of this research is to investigate and evaluate the existing work related to the dynamic s-box. Other than that, the aim of this research is to design a dynamic s-box using affine transformation in order to increase the security of the encryption. The method to design is using java with the NetBeans software. The proposed block cipher will be tested using NIST statistical test suite to test the randomness of the algorithm. Besides, the strength of the s-box will be analyzed using the s-box evaluation tool (set). The cryptographic strength depends strongly on the choice of s-box. Therefore, this new proposed block cipher can be used by countries, organizations, stakeholders, or interested parties as one of the secure algorithms to increase the protection of the information and also will contribute as an alternative to other cryptographic algorithms in computer security research.

Keywords:

Box, AES, Affine Transformation, NIST, Cryptography

Introduction

Cryptography is defined as a method or technique of secure communication in the presence of third party. In modern age of computers, cryptography is a technique to scramble plaintext or original message into ciphertext by using cryptographic algorithms. Cryptography is an effective way of protecting sensitive information whether it is stored in media or transmitted. The main objective in cryptography is to attain data confidentiality, data integrity, authentication and non-repudiation.

Cryptography is generally divided into two categories which are known as symmetric key cryptography and asymmetric key cryptography. Symmetric key cryptography is a form of cryptosystem in which encryption and decryption are performed using the same key (Forouzan, 2008). Symmetric encryption transforms plaintext into ciphertext using a secret key and an encryption algorithm. It is widely used in symmetric ciphers for instance DES (Data Encryption Standard) and AES (Advanced Encryption Standard).

Asymmetric key encryption is a form of cryptosystem in which encryption and decryption are performed using the different keys. One is public key for encryption and the other one is private key for decryption. Asymmetric encryption can be used for confidentiality, authentication or both. The most widely used asymmetric key encryption are Rivest-Shamir-Adleman (RSA), Diffie-Hellman key exchange, ElGamal Cryptosystem and Elliptic Curve Cryptography (ECC) (Amandeep, 2017). The asymmetric algorithm as outlined in the Diffie-Hellman paper uses numbers raised to specific powers to produce decryption keys. RSA is the most widely used asymmetric algorithm is embedded in the Secure Socket Layer/Transport Layer Security (SSL/TLS) protocol which is used to provide communications security over a computer network.

Literature Review

Advanced Encryption Standard (AES)

AES is designed on the principle of combination of both substitution and permutation. The AES fixes the block length to 128 bits and supports key lengths of 128, 192 or 256 bits. It represented in matrix form called state matrix and operated on 4x4 column-major order matrix of bytes. Most AES calculations are done in a special finite field $GF(2^8)$. The element of a finite field can be represented in several different ways. For any prime power there is a single finite field, hence all representations of $GF(2^8)$ are isomorphic. Despite this equivalence, the representation has an impact on the implementation complexity. A byte b , consisting of bits $b_7 b_6 b_5 b_4 b_3 b_2 b_1 b_0$, is considered as a polynomial with coefficient in $\{0,1\}$:

$$b_7 x^7 + b_6 x^6 + b_5 x^5 + b_4 x^4 + b_3 x^3 + b_2 x^2 + b_1 x + b_0$$

Example: the byte with hexadecimal value '57' (binary 0101 0111) corresponds with polynomial

$$x^6 + x^4 + x^2 + x + 1.$$

The key size used for AES specifies the number of repetitions of transformation rounds. The number of cycles of repetition is as follows (Daeman, 2002):

- i. 10 cycles of repetition for 128-bit keys.
- ii. 12 cycles of repetition for 192-bit keys.
- iii. 14 cycles of repetition for 256-bit keys.

Encryption process follows four (4) following steps which are

- i. The ByteSub Transformation

The ByteSub Transformation is a non-linear byte substitution, operating on each of the State bytes independently. SubBytes is a bricklayer permutation consisting of an S-Box applied to the bytes of the state. Figure 1 below illustrates the effect of the SubBytes step on the state.

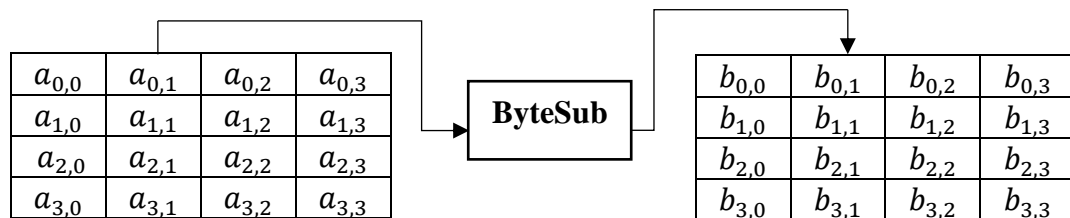


Figure 1: SubBytes Transformation

The substitution table or known as S-Box is invertible and is constructed by the composition of two transformations:

- a. First, taking the multiplicative inverse in $GF(2^8)$.
- b. Then, applying an affine transformation defined by:

$$\begin{bmatrix} y_0 \\ y_1 \\ y_2 \\ y_3 \\ y_4 \\ y_5 \\ y_6 \\ y_7 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 1 & 1 & 0 & 0 & 0 & 1 & 1 & 1 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 1 \\ 1 & 1 & 1 & 1 & 0 & 0 & 0 & 1 \\ 1 & 1 & 1 & 1 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 1 & 1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 1 & 1 \end{bmatrix}$$

The affine transformation can also be described as a polynomial multiplication, followed by the XOR with a constant (Rijndael, 2002).

$$\text{S-Box of AES is generated by equation: } Y = Ax \oplus c \text{ mod } M \quad (1)$$

Where 'A' is represented as affine matrix, 'x' is a vector that is multiplicative inverse of element of state matrix, 'c' is affine constant 63 (01100011) and 'M' is irreducible polynomial $x^8 + x^4 + x^3 + x + 1$. The S-Box generated by equation (1) is represented in the Table 1 below.

Table 1: AES S-Box

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	63	7C	77	7B	F2	6B	6F	C5	30	01	67	2B	FE	D7	AB	76
1	CA	82	C9	7D	FA	59	47	F0	AD	D4	A2	AF	9C	A4	72	C0
2	B7	FD	93	26	36	3F	F7	CC	34	A5	E5	F1	71	D8	31	15
3	04	C7	23	C3	18	96	05	9A	07	12	80	E2	EB	27	B2	75
4	09	83	2C	1A	1B	6E	5A	A0	52	3B	D6	B3	29	E3	2F	84
5	53	D1	00	ED	20	FC	B1	5B	6A	CB	BE	39	4A	4C	58	CF
6	D0	EF	AA	FB	43	4D	33	85	45	F9	02	7F	50	3C	9F	A8
7	51	A3	40	8F	92	9D	38	F5	BC	B6	DA	21	10	FF	F3	D2
8	CD	0C	13	EC	5F	97	44	17	C4	A7	7E	3D	64	5D	19	73
9	60	81	4F	DC	22	2A	90	88	46	EE	B8	14	DE	5E	0B	DB
A	E0	32	3A	0A	49	06	24	5C	C2	D3	AC	62	91	95	E4	79
B	E7	C8	37	6D	8D	D5	4E	A9	6C	56	F4	EA	65	7A	AE	08
C	BA	78	25	2E	1C	A6	B4	C6	E8	DD	74	1F	4B	BD	8B	8A
D	70	3E	B5	66	48	03	F6	0E	61	35	57	B9	86	C1	1D	9E
E	E1	F8	98	11	69	D9	8E	94	9B	1E	87	E9	CE	55	28	DF
F	8C	A1	89	0D	BF	E6	42	68	41	99	2D	0F	B0	54	BB	16

i. The ShiftRow Transformation

The ShiftRows step is a byte transposition that cyclically shifts the rows of the state over different offsets. In AES, the first row is left unchanged. Each byte of the second row is shifted one to the left. Similarly, the third and fourth rows are shifted by offsets of two and three respectively. For example we get s' matrix from state matrix s after shift row in following transformation:

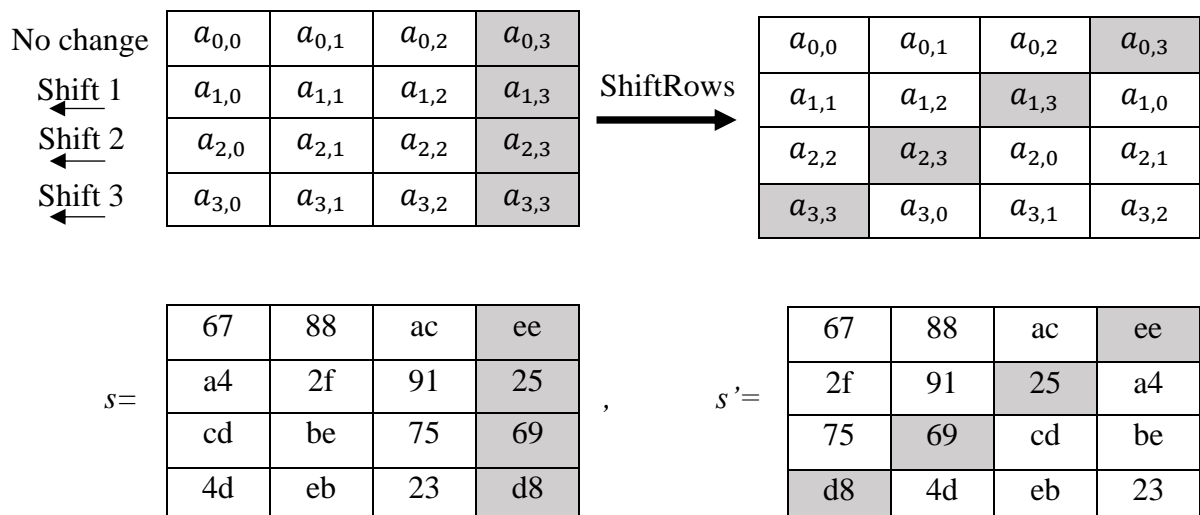


Figure 2: ShiftRows Transformation

ii. The MixColumns Transformation

The MixColumns step is a bricklayer permutation operating on the column by column. In MixColumn, column vector is multiplied with a fixed matrix, where the bytes are treated as a polynomial rather than numbers.

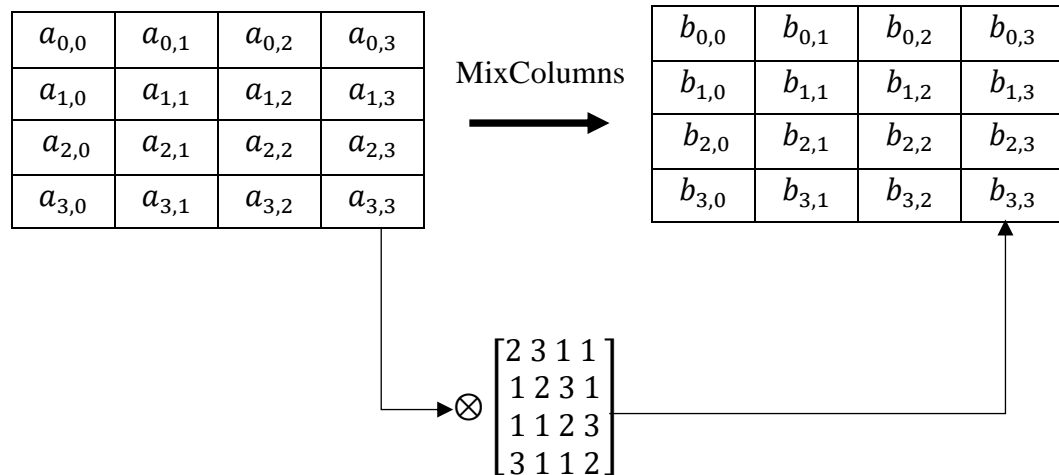


Figure 3: MixColumns Transformation

iii. Add Round Key

The Add Round key operation is an XOR operation between the State and the Round Key. By doing XOR operation element by element of both State and Round Key, next state of matrix is obtained.

$$\text{State}'(a,b) = \text{State}(a,b) \oplus \text{Round Key}(a,b)$$

$$\text{State} = \begin{bmatrix} 32 & 88 & 31 & e0 \\ 43 & 5a & 31 & 37 \\ f6 & 30 & 98 & 07 \\ a8 & 8d & a2 & 34 \end{bmatrix}, \text{Round Key} = \begin{bmatrix} 00 & a5 & a8 & a0 \\ e9 & 09 & bb & 2a \\ c9 & d4 & b7 & ab \\ f2 & e8 & 60 & 08 \end{bmatrix}$$

The new state will be:

$$32(\text{hex}) \oplus 00(\text{hex}) = 0011\ 0010 \oplus 0000\ 0000 \\ = 0011\ 0010 (32)$$

$$88(\text{hex}) \oplus a5(\text{hex}) = 1000\ 1000 \oplus 1010\ 0101 \\ = 0010\ 1101 (2d)$$

$$\text{State}' = \begin{bmatrix} 32 & 2d & 99 & 40 \\ aa & 53 & 8a & 1d \\ 3f & e4 & 2f & ac \\ 5a & 65 & c2 & 3c \end{bmatrix}$$

S-Box Properties

The strength of block ciphers which work on substitution and permutation like AES is basically depends on the construction of S-Box. The S-Box maps an 8-bit input to an 8-bit output. Both the input and output are interpreted as polynomials over $GF(2)$. First, the input is mapped to its multiplicative inverse in $GF(2^8) = GF(2)[x]/x^8 + x^4 + x^3 + x + 1$. The AES S-Box was specifically designed to be resistant to linear and differential cryptanalysis. This was done by minimizing the correlation between linear transformations of input/output bits, and at the same time minimizing the difference propagation probability. The AES S-Box can be edited which defeats the suspicion of a backdoor built into the cipher that exploits a static S-Box.

The properties of S-Box have been widely used as a base of new encryption technique for instance nonlinearity, differential uniformity and strict avalanche criterion (Zhang, 2013).

- i. Bijection – requires a one-to-one and onto mapping from input vectors to output vectors if the S-Box is n by n bit.
- ii. Strict avalanche criterion – occurs if one input bit i is changed, each output bit will change with probability of one half. Strict avalanche requires that if there are any slight changes in the input vector, there will be a significant change in the output vector. To achieve this effect, we will need a function that has a 50% dependency on each of its n input bits.
- iii. Bit independence criterion or correlation immunity – requires that output bits act independently from each other's. In other words, there should not be any statistical pattern or statistical dependencies between output bits from the output vectors.
- iv. Nonlinearity – requires that the S-Box is not a linear mapping from input to output. This would make the cryptosystem susceptible to attacks (Coppersmith, 1994). If the S-Box is constructed with maximally nonlinear Boolean functions, it will give a bad approximation by linear functions thus making a cryptosystem difficult to break. The nonlinearity of an S-Box must be high to resist against linear cryptanalysis.
- v. Balance – means that each Boolean vector responsible for the S-Box has the same number of 0's and 1's. The significance of the balance property is based on the higher the magnitude of function imbalance, a high probability linear approximation being obtained.
- vi. Differential uniformity – the smaller is the differential uniformity, the better is the S-Box's resistance against differential cryptanalysis.

Conclusion

AES has been designed to have a very strong resistance against the classical attacks such as linear cryptanalysis and differential cryptanalysis. However, since AES is very algebraic, new algebraic attacks was appeared (Ferguson, 2001). Therefore, the need for design dynamic S-Box which is key-dependent will help to increase the security of encryption. Most of researchers aware that S-Box properties are very crucial in order to make the S-Box strong enough and secure. However, most of them analyse their S-Box properties based on their perceptions without any proper guideline. In addition, cryptanalysis attempt to break the S-Box properties with all kinds of methods.

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